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Dillon et al.

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(54) **METHOD AND APPARATUS FOR
CONNECTING COMPUTER TO SATELLITE
ANTENNA**

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(List continued on next page.)

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Partial Translation of Japanese Utility Model No. 4-43074.
Partial Translation and Abstract of Japanese Laid-Open
Patent Application No. 7-336261.

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1997, now Pat. No. 6,125,184, which is a division of
application No. 08/557,398, filed on Nov. 13, 1995, now Pat.
No. 5,652,795, which is a continuation-in-part of application
No. 08/340,347, filed on Nov. 14, 1994, now abandoned.

(51) **Int. Cl.⁷** H04B 1/16

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(58) **Field of Search** 370/392; 375/316;
380/44

(57)

ABSTRACT

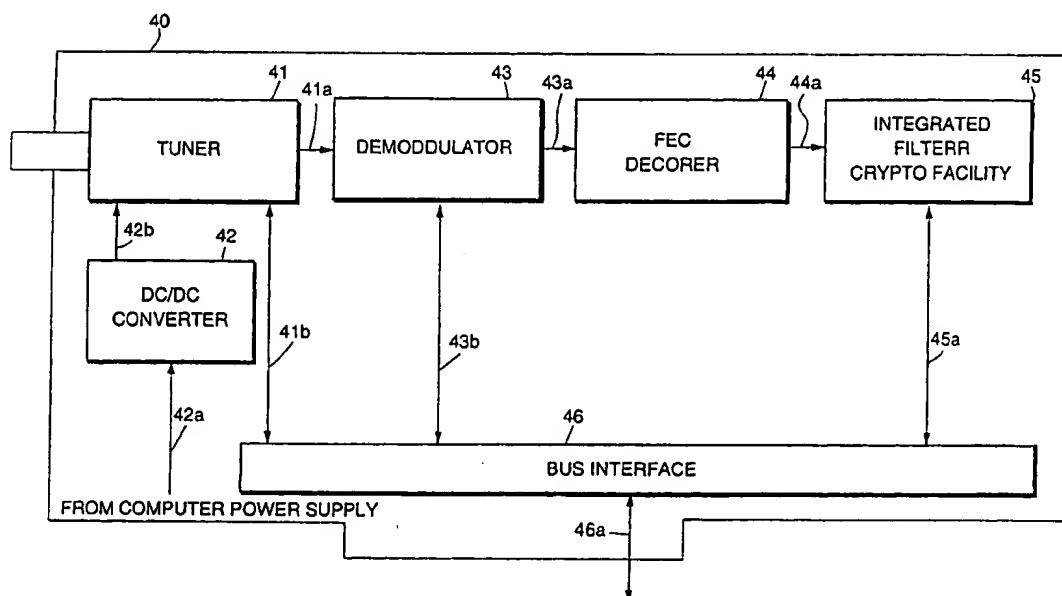
A method and apparatus for an adapter card, for use in a
computer, to provide conditional access by the computer to
incoming data streams while maintaining the security of
information by maintaining a listing of addresses corre-
sponding to data streams which the computer is authorized
to receive, receiving and determining the address of a frame,
determining whether the frame address matches an address
maintained in an Access Table, and processing and trans-
mitting only those frames of data streams which the com-
puter is authorized to receive.

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49 Claims, 2 Drawing Sheets



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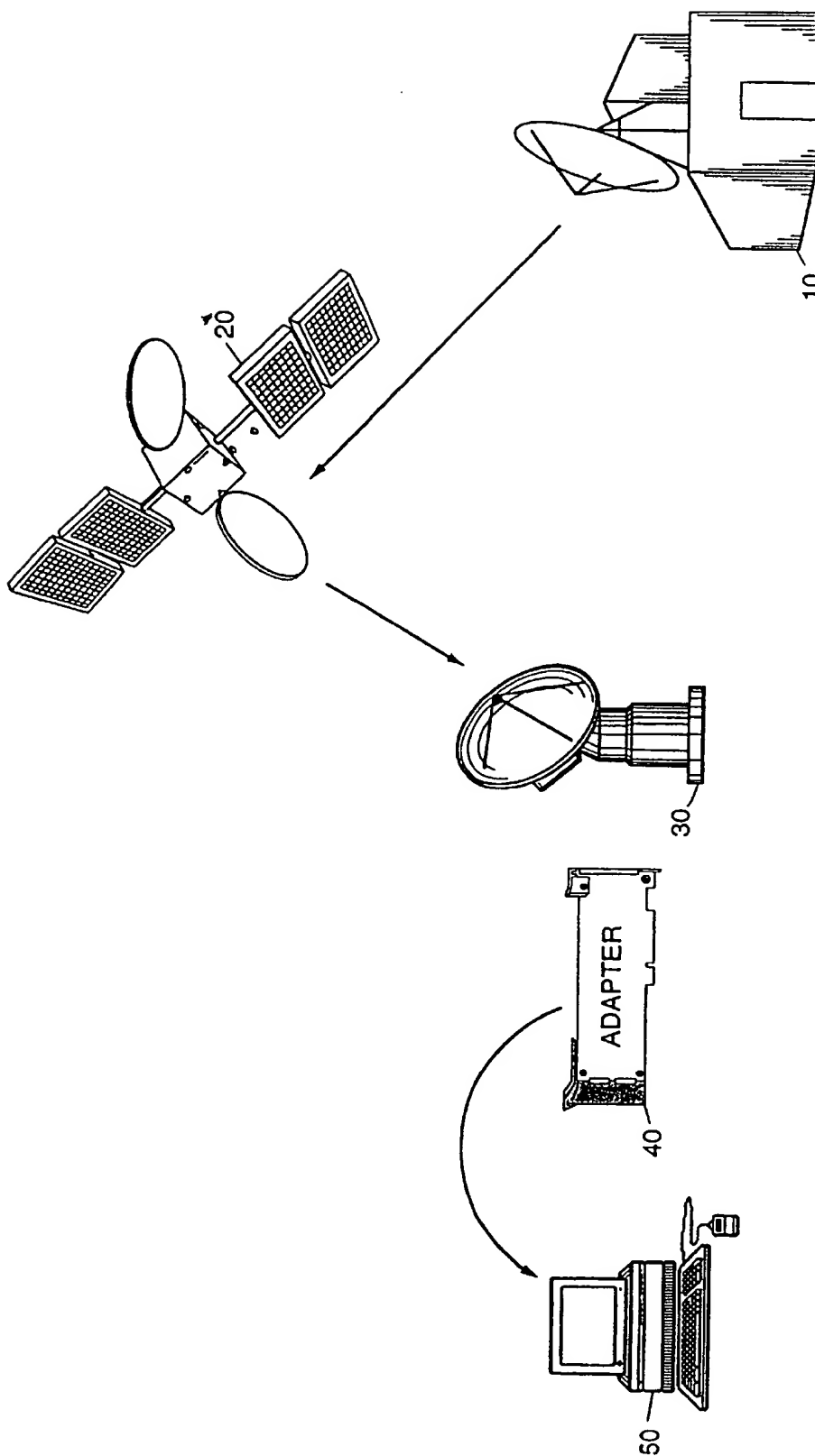


FIG. 1.

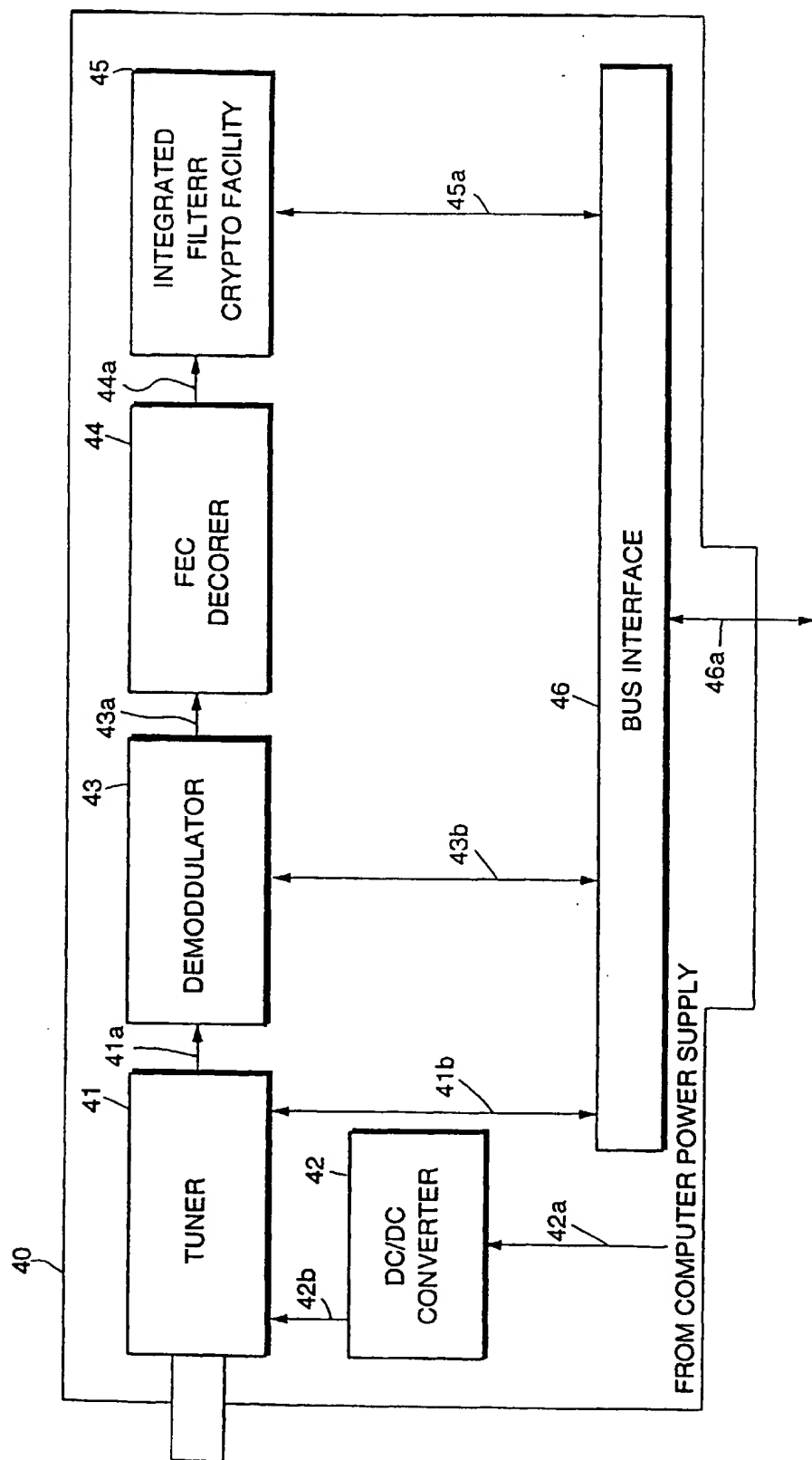


FIG. 2.

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METHOD AND APPARATUS FOR CONNECTING COMPUTER TO SATELLITE ANTENNA

RELATED APPLICATIONS

This application is a division of application Ser. No. 08/862,346 filed May 23, 1997, U.S. Pat. No. 6,125,184 which is a division of application Ser. No. 08/557,398 filed Nov. 13, 1995, U.S. Pat. No. 5,652,795, which is a continuation-in-part of application Ser. No. 08/340,347 filed Nov. 14, 1994, now abandoned.

FIELD OF THE INVENTION

The present invention relates generally to the field of communications, and more specifically to conditional access by computers to data streams transmitted via satellite.

BACKGROUND OF THE INVENTION

Satellite communication has long been used to broadcast data to computers. In situations requiring the same data to be transmitted to multiple sites, satellite communication, which allows a single transmission of data to be received at multiple sites, can be more cost effective than terrestrial communication, which requires separate transmission to each site.

In the past, data broadcast via satellite to a computer typically was received by an external satellite receiver, which serially passed a single data stream to the computer through a serial adapter interface. An example of such a system is the Opportunistic Data File Broadcast system developed by Hughes Network Systems, which employs a Compression Labs Spectrum Saver satellite receiver to pass RS-422 serial data to high-speed adapters in computers at remote sites. This particular system has been used by financial institutions to broadcast financial data from central data centers to branch offices.

For commercial data broadcasts, conditional access technology enabling only authorized users to access the broadcast data is essential to ensure payment for the data. Conditional access is typically implemented by encrypting the broadcast data and providing the decryption keys only to authorized receivers. In prior satellite data communication systems, such as the Opportunistic Data File Broadcast system, conditional access functions have been performed by the external satellite receiver, which outputs a single decrypted data stream to the receiving computer. In these systems, conditional access is provided for only a single data stream. Thus, to access multiple data streams simultaneously transmitted would require users to have a complete set of receiving equipment for each data stream.

Another type of satellite communication system has been suggested in which an adapter card, insertable into a computer, enables satellite reception without the need for the external satellite receiver. Examples of such systems are disclosed in U.S. Pat. No. 4,777,657 to Gillaspie, U.S. Pat. No. 5,019,910 to Filmer, and U.S. Pat. No. 5,359,367 to Stockill.

However, these references fail to teach a system incorporating conditional access technology, and similarly fail to provide for simultaneous receipt of multiple data streams where the data streams are individually conditional access controlled. Furthermore, to the extent that the prior art discloses use of decryption hardware, none of the prior art systems extends the use of the hardware beyond decrypting an incoming broadcast data stream to providing encryption and decryption operations for the connected computer.

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SUMMARY OF THE INVENTION

The present invention addresses the shortcomings of prior art systems by teaching a secure satellite communication system for providing subscribers with conditional access to broadcast satellite data through their computers. This system maintains the security of information by providing access only to authorized users, and provides an additional benefit by enabling users to concurrently receive and decrypt multiple data streams.

In a preferred embodiment, the present invention teaches a method for an adapter card, for use in a computer, to provide conditional access by the computer to input data, including data streams transmitted via satellite, wherein a data stream is comprised of at least one frame and each frame includes an address identifying the data stream. This method includes the steps of maintaining an Access Table, in which each table entry includes an address field for storing an address corresponding to a data stream which the computer is authorized to receive, receiving a frame and determining its address, and determining whether the frame address matches an address maintained in the Access Table in order to determine whether the computer has access to the data stream corresponding to the frame. If a match is not found, the adapter card discards the frame, but if a match is found, the adapter card processes and transmits the frame to the computer through a bus interface.

In a preferred embodiment, the adapter card additionally is capable of processing data transmitted from the computer.

In addition to the method described above, the present invention is also directed to an adapter card for use in a computer, for providing conditional access by the computer to input data including frames of data streams transmitted via satellite, wherein each data stream is comprised of at least one frame and each frame includes an address identifying the data stream. The adapter card includes a receiver for receiving a data stream frame, an Access Table, in which each table entry includes an address field for storing an address corresponding to a data stream which the computer is authorized to receive, an integrated filter/crypt (IFC) block for determining an address of the received frame and determining whether the frame address matches an address maintained in the Access Table, and a bus interface for enabling communication between the adapter card and the computer. If the IFC finds that the frame address does not match a table address, the IFC block discards the frame, but if it finds a match, the IFC block processes the frame into output data, to be transmitted to the computer via the bus interface. The IFC preferably is also capable of processing data input from the computer into output data.

The invention itself, together with further objects and attendant advantages, will best be understood by reference to the following detailed description taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is an illustration of one form of satellite broadcast system in which the present invention may be implemented.

FIG. 2 is a block diagram of the preferred embodiment of the adapter card receiver of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

It is envisioned that the present invention will be used in conjunction with a satellite broadcast system such as that shown in FIG. 1. As illustrated, this system includes a

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network operations center (NOC) 10, satellites 20, antenna receivers 30, adapter card receivers 40 (adapter cards), and computers 50.

In this system, a subscriber, through an adapter card 40 inserted in a computer 50, receives information in data streams broadcast via satellite 20. The data streams are uplinked to one or more communications satellites 20 by the NOC 10 from a large antenna broadcast earth station. Each data stream is broadcast in units of frames, with each frame including an address header for identifying the data stream, a data field containing data, and a CRC tail for ensuring accurate transmission. Preferably, the frames are encrypted, and the key needed to decrypt the data of a frame is identified by its address header. Accordingly, data can be accessed only by receivers having the proper key.

Each antenna receiver 30, comprising a smaller antenna and low-noise block (LNB) element, receives the broadcast frames and transmits the frames to a connected adapter card 40 inserted in the subscriber's computer 50. Because the subscriber may have access to only a subset of the broadcast data streams, the adapter card 40 screens incoming frames, and processes only those frames the subscriber is authorized to receive by using the frame address field to retrieve the corresponding key for decrypting each frame. The adapter card 40 then transmits the decrypted frames through a bus interface to the computer 50.

The present invention is directed to the adapter card 40, which provides the conditional access to incoming data streams, and preferably enables concurrent reception of multiple data streams. By processing only authorized frames and discarding all others, the present invention prevents unauthorized frames from ever being passed to the computer 50. This not only prevents subscribers from illicitly accessing the decrypted data stream, but also provides a physical measure of security by preventing the encrypted data stream from being made available to be downloaded into the computer 50. This is an important feature because security mechanisms have been evaded by downloading encrypted data, as once the encrypted data stream is stored in the computer memory, the user can access the data for as many attempts as needed for decryption.

By evaluating and decrypting incoming data streams on a frame-by-frame basis, the present invention enables simultaneous reception of multiple conditional access controlled data streams statistically multiplexed in a received signal. Furthermore, because the present invention further enables frames to be decrypted as they are received, the need for buffer RAMs required in prior systems to store incoming frames can be substantially reduced or eliminated, thereby lowering the cost of the systems.

As explained in greater detail below, the preferred embodiment of the present invention increases the security of the conditional access provided to the transmitted data streams by providing a secure management system for the decryption keys. More specifically, the preferred embodiment provides a system having at least two levels of encryption, in which the decryption keys are themselves encrypted and never appear to users in decrypted or human-intelligible form.

In the preferred embodiment, the adapter card 40 additionally provides the encrypting and decrypting capabilities of the adapter card 40 to be used by the computer 50 as a general purpose bulk data encryptor/decryptor, even while the adapter card 40 is concurrently processing received frames. Not only does this feature reduce the overall cost to the user by eliminating the need for a separate data

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encryption/decryption engine to handle computer data, it also permits the advantageous conditional access features of the present invention to be provided to data received by the adapter card 40 from the computer 50 via a number of possible sources, rather than from the satellite 20 alone.

FIG. 2 illustrates the adapter card 40 of the preferred embodiment, which includes the following functional blocks:

1. Front End Block 41, 42: connects the adapter card receiver 40 to the antenna receiver 30, for receiving a plurality of signals from the NOC 10 via the satellite 20 and transmitting data streams 41a from a particular signal selected by the tuner 41 to the Demodulator Block 43;
2. Demodulator Block 43: converts data streams 41a received on the selected signal into digital form and transmits the demodulated data stream 43a to the FEC Decoder Block 44;
3. FEC Decoder Block 44: identifies and corrects transmission errors in the demodulated data stream 43a and transmits the corrected data stream 44a to the Integrated Filter/Crypto Block 45;
4. Integrated Filter/Crypto Block 45: identifies frames within the received data stream 44a and decrypts frames of the accessible data streams; and
5. Bus Interface Block 46: enables communication between the adapter card 40 and the computer 50.

1. Front End Block

In the preferred embodiment, as illustrated in FIG. 2, the Front End Block includes a tuner 41, which can be set to receive an IF signal in 100 kHz steps in the frequency range of 950 to 1450 MHz, and downconverts the received signal to an IF output centered at 60 MHz. The operation of the tuner 41 can be controlled 41b by the computer 50 through the Bus Interface Block 46, or in a preferred embodiment, a local microprocessor may be provided on the adapter card 40 to monitor and control the tuner 41.

The Front End Block preferably also comprises a DC—DC converter 42, to provide all power required by the tuner 41 as well as the attached LNB by converting a +5V signal 42a received from the computer power supply to voltages required by the various components (e.g., 42b).

2. Demodulator Block

The Demodulator Block 43 receives and demodulates the downconverted signal 41a from the Front End Block. In the preferred embodiment, the Demodulator Block 43 is implemented in a CMOS ASIC, and, through control 43b by the computer 50 or an adapter card microprocessor, provides all baseband signal processing required for demodulation.

3. FEC Decoder Block

The FEC Decoder Block 44 uses redundant data in the demodulated digital data stream 43a to identify and correct transmission errors. In the preferred embodiment, FEC Decoder Block 44 consists of a Viterbi soft-decision decoder, depuncture logic which allows the rate $\frac{1}{2}$ Viterbi to operate at an effective rate of $\frac{2}{3}$, a deinterleaver which scatters the errors resulting from the incorrect attempts to correct data, and a Reed-Solomon decoder capable of correcting the scattered errors.

4. Integrated Filter and Crypto Facility Block

The Integrated Filter and Crypto Facility (IFC) Block 45 plays a primary role in enabling the inventive conditional access features provided.

The IFC 45 receives frames of the digital data streams 44a, which are preferably encrypted. In the preferred embodiment, the standard DES algorithm is used, although alternative encryption algorithms are possible as well.

There are numerous methods by which users can acquire decryption keys, and the present invention can be adapted for use with any of these methods. For example, a subscriber can obtain a "smart card" from the service provider, typically a credit-card sized card readable by the computer. In some cases, the information provider can send information that passes into the smart card to generate the decryption keys. Another method is to obtain the keys through messages received from the satellite link or over a terrestrial communications system (e.g., via a modem connected to the computer). Still another method is for the subscriber to obtain the keys orally from the information provider and to manually enter the keys into the computer through the keyboard input.

The security of the present invention can be maintained with any of these systems because it does not depend solely on the security of the actual transmission of the keys. Rather, the security system of the present invention has multiple levels of security. At a first level, to receive any satellite data stream, a user must subscribe to the service provider and must obtain the necessary wiring and equipment. At the next level, security is enhanced by means of "groups." Each group comprises a set of data streams, and a subscriber must have access to a particular group in order to receive any of its data streams. Finally, in the preferred embodiment, an additional level may be provided by requiring subscribers to have specific authorization to receive each individual data stream within a group.

The present invention implements this tiered conditional access system through key management. Users become subscribers by subscribing to the service provider and obtaining an adapter card receiver 40, having an IFC 45, to be inserted into a computer 50. In the preferred embodiment, the IFC 45 is physically secured by implementation in an ASIC, which is equipped with a burned-in Master Key used for identification purposes. In the preferred embodiment, a unique Master Key is used for each IFC 45.

Preferably, to enhance the security of the conditional access features provided, the Master Key is used for subscriber identification rather than decryption. Accordingly, each adapter card 40 is also assigned a unique User Key which is encrypted with the Master Key before being distributed to the subscriber, and is decrypted only within the IFC 45. This prevents the User Key from ever appearing in unencrypted form, even to the subscriber. Use of the User Key rather than the Master Key allows the Master Key to be kept physically secure within the ASIC and minimizes its use, thereby minimizing its exposure. Consequently, the only method of compromising the security of the Master Key is by breaking the algorithm used to encrypt the User Key or by compromising the physical security of the IFC 45. Furthermore, the security of the overall system can be maintained through key distribution by simply changing a subscriber's User Key. This avoids the need for physical replacement of the IFC 45 even if the security of the User Key is compromised.

In the preferred embodiment having group access requirements, the function of the User Key is to decrypt Group Keys. A subscriber wanting to access any data stream within a particular group must have the Group Key for that group. A Group Key can be distributed to subscribers by any appropriate means with little risk to the security of the

system because the entity providing distribution of Group Keys will encrypt a Group Key with the subscriber's unique User Key before transmission. Accordingly, the Group Key can be decrypted only by the IFC 45 of the subscriber for which it is intended, minimizing the likelihood that it will be distributed to and successfully used by an unauthorized user. Furthermore, even the authorized subscriber will never have access to the decrypted Group Key, because the Group Key is encrypted by the User Key until decrypted within the physically secure IFC ASIC, and as described above, the subscriber does not have access to the User Key.

In the preferred embodiment, the only function of a Group Key is to decrypt Data Keys for the data streams within the group. As discussed, because a subscriber must be a member of a group to access any of the data streams within the group, only subscribers having the appropriate Group Key can decrypt the Data Key. Additionally, because each Data Key is encrypted with its Group Key before being distributed to subscribers, in order to access and decrypt a particular data stream within a group, the subscriber must have the appropriate Data Key for that data stream.

The combination of the Group Keys and the Data Keys determine the subscriber's accessibility to the incoming data streams, and must be kept current. In the preferred embodiment, an Access Table is stored in adapter card memory outside of the IFC 45, with entries corresponding to data streams the subscriber is authorized to receive. Each entry accordingly has fields for an address of a data stream the subscriber is authorized to access, and a Group Key and a Data Key required to decrypt that data stream. Because this table is located outside the IFC 45, the Group and Data Keys are stored in encrypted form in the Access Table. A micro-processor is preferably provided on the adapter card 40 outside the IFC 45 to perform the table functions and to indicate to the IFC 45 whether a particular address matches a table entry, and also to indicate the appropriate key, if a match is found.

In the preferred embodiment, other aspects of the key management system are implemented within the IFC ASIC. For example, logic is provided for maintaining and storing current keys, as well as for handling data in both encrypted and decrypted form. As earlier noted, the IFC 45 in a preferred embodiment provides decryption functions for incoming frames as well as encryption and decryption functions for bulk data from the computer 50. To provide these features, the ASIC logic includes at least the following subblocks:

- UK Register, for storing the User Key of the subscriber, which as described above, is decrypted by the Master Key as it is loaded into the IFC 45;
- GK Register, for storing a single Group Key. Although a subscriber may have access to more than one group, as described above, the preferred embodiment provides an access table outside the IFC 45 for storing the Group Keys in encrypted form. When the IFC 45 receives an encrypted frame, it extracts the data stream address, and if the address matches one of the access table entries, the appropriate Group Key is decrypted by the User Key as it is loaded into the IFC 45;
- DK Register, for storing a Data Key to decrypt input data, whether satellite data or computer data;
- an encryption/decryption engine, for performing the actual decryption of satellite data, or encryption or decryption of computer data from other sources;
- Input Registers, for storing input yet to be processed, preferably including at least one register specifically

designated for satellite data and at least one register specifically designated for computer data;

Opcode Registers, for indicating the location of the appropriate key for a particular operation and the location where the output should be stored, preferably including at least one register specifically designated for satellite data and at least one register specifically designated for computer data; and

Output Registers, preferably including at least one register specifically designated for satellite data and at least one register specifically designated for computer data from other sources.

In the preferred embodiment, the logic is implemented to provide functions for loading Group Keys, loading Data Keys, processing satellite data, and processing computer data. The Load Group Key function is used to provide the IFC 45 with the necessary Group Key for decryption, when the address of a received frame matches an address stored in the access table. Input to the Load Group Key function is a Group Key encrypted by the User Key. When the IFC 45 receives this instruction, it retrieves the User Key from the UK Register to decrypt the received Group Key, and stores the decrypted Group Key in the GK Register.

A Load Data Key instruction indicates to the IFC 45 that the input is a Data Key encrypted by a Group Key. In response, the IFC 45 will decrypt the input with the Group Key stored in the GK Register and store the resulting decrypted Data Key in the DK Register.

When the IFC 45 receives a Process Satellite Data instruction, the input contains an incoming satellite data stream frame. The IFC 45 locates the address in the address header of the frame and determines whether it has the Data Key necessary for decryption by searching the access table for the frame address. If a match is found, the IFC 45 will retrieve the appropriate Group and Data Keys, decrypt the frame and store the decrypted frame in an Output Buffer for satellite data; if not, the IFC 45 will discard the frame.

Process Computer Data instructions are handled similarly. As previously noted, in the preferred embodiment, the IFC encryption/decryption engine is capable of encrypting and decrypting data from other sources, transmitted by the computer 50. To enable operations to be simultaneously performed on both incoming satellite frames as well as incoming computer data, the IFC 45 time shares between frame reception and data encryption and decryption operations. To process computer data, the computer 50 must transmit the key to be used, an indication of whether the key is to be used for encryption or decryption, and the data on which the operation is to be performed. Based on this input, the IFC 45 will perform the appropriate operation on the data and return the processed data to the attached computer 50 via the Bus Interface Block 46.

Thus, unlike systems described in prior art which simply transmit incoming data streams into the computer 50, the IFC 45 of the present invention allows the computer 50 only conditional access to received data streams. In particular, only authorized signals are ever downloaded into the computer 50. The preferred embodiment provides enhanced security by ensuring that none of the keys required at any level of decryption ever appears outside the IFC ASIC in decrypted form, and by providing multiple layers of security. These layers not only make unauthorized access more difficult, but by implementing the layers in the key management system, they also enable a fast, simple method to recover from most security problems. More specifically, in response to problems due to an unauthorized user obtaining a User Key assigned to an authorized subscriber, a new User

Key can be distributed to that subscriber. Similarly, in response to security problems caused by unauthorized access to particular data streams, the data stream and group keys required for decrypting those data streams can be changed, and the updated keys provided only to authorized users.

Besides security, the IFC 45 provides data management benefits by efficiently using memory and computer processing time, which is especially useful in downloading large files. The present invention filters incoming data stream frames by determining whether the computer is authorized to receive the frame based on its address, accepting only authorized frames. This significantly reduces the adapter memory requirements, since individual frames are decrypted as they are received rather than requiring encrypted frames to be buffered in their entirety awaiting decryption. This feature may eliminate the need for buffering frames on the adapter entirely, eliminating the need for buffers on the adapter card 40 and providing a significant cost reduction.

Yet another advantage provided by the IFC 45 of the present invention is that, by providing address-based frame decryption, the adapter card 40 can simultaneously decrypt multiple data streams that are statistically multiplexed on the same broadcast signal.

One skilled in the art will recognize that variations for providing the required capabilities of the IFC 45. For example, although the preferred embodiment is implemented with the three level security scheme described above, the present invention may be implemented with only two levels of security by providing only the User Keys and the Data Keys. In this case, the User Key serves the same purpose as in the three-level scheme, but the Data Keys will be provided to the adapter card 40 encrypted in the User Key rather than in a Group Key. Other levels of security may likewise be used without departing from the claimed invention.

5. Bus Interface Block

The Bus Interface Block 46 enables communication 46a between the computer and the adapter card 40 for, among other things, controlling and monitoring the tuner 41 and the demodulator 43 and maintaining the key management system. Additionally, the Bus Interface Block 46 allows the attached computer 50 to obtain decrypted frames from the IFC Block 46, and to pass bulk data to the IFC 46 for encryption or decryption and receive the resulting processed data. After performing the required operation on incoming data, one implementation buffers the resulting data in memory provided on the adapter card 40, and interrupts the attached computer 50 for retrieval. Alternatively, in a preferred embodiment, the Bus Interface Block 46 automatically reads the decrypted data into the memory of the attached computer 50.

The present invention, as illustratively embodied in the preferred embodiment described above, provides an adapter card receiver 40, to be used in conjunction with a connected computer 50, for enabling users to conditionally access and quickly download multiple data streams, such as signals transmitted via satellite 20. The invention also maintains the security of privileged information by preventing the information to enter the computer of unauthorized users.

However, it should be understood that a wide range of changes and modifications can be made to the preferred embodiment described above. It is therefore intended that the foregoing detailed description be illustrative rather than limiting, and that it is the following claims, including all equivalents, which are intended to define the scope of this invention.

What is claimed is:

1. An apparatus comprising:

- a connector that is configured to connect said apparatus to an antenna device comprising an antenna capable of receiving a signal from a satellite;
- a tuner that is configured to receive a satellite signal from said connector and to perform tuning on the satellite signal;
- a demodulator that is configured to receive the tuned satellite signal from said tuner and to perform demodulation on the tuned satellite signal, thereby outputting a demodulated data stream, wherein the demodulation is performed in accordance with control by a computer;
- a forward error corrector that is configured to receive the demodulated data stream from said demodulator and to perform forward error correction on the demodulated data stream, thereby outputting a corrected data stream;
- an address filterer that is configured to receive the corrected data stream from said forward error corrector and to perform address filtering upon frames within the corrected data stream, thereby outputting frames selected in accordance with the address filtering; and
- an interface that is configured to receive the selected frames from said address filterer and to output the selected frames to the computer.

2. An apparatus according to claim 1, wherein said apparatus is insertable into the computer.

3. An apparatus according to claim 2, wherein said apparatus is an adapter card.

4. An apparatus according to claim 3, wherein said apparatus outputs the selected frames to the computer through a bus interface.

5. An apparatus according to claim 1, wherein each frame comprises an address header for identifying the data stream to which the frame belongs, a data field containing data, and a CRC field.

6. An apparatus according to claim 1, wherein the antenna device further comprises a low noise block, and said connector is configured to provide power to the low noise block using power from the computer.

7. An apparatus according to claim 6, wherein said connector is configured to provide power to the low noise block by using a DC-DC converter to convert power from the computer.

8. An apparatus according to claim 7, wherein the DC-DC converter provides power to said tuner.

9. An apparatus according to claim 1, wherein said forward error corrector comprises a Viterbi soft-decision decoder, depuncture logic, a deinterleaver, and a Reed-Solomon decoder.

10. An apparatus according to claim 1, wherein said computer is controlled by the computer.

11. An apparatus according to claim 1, wherein the computer monitors said demodulator.

12. An apparatus according to claim 1, wherein said interface interrupts the computer to retrieve the selected frames.

13. An apparatus according to claim 1, wherein said interface automatically transfers the selected frames into memory of the computer.

14. An apparatus comprising:

- a connector that is configured to connect said apparatus to an antenna device comprising an antenna capable of receiving a signal from a satellite;
- a tuner that is configured to receive a satellite signal from said connector and to perform tuning on the satellite signal;

a demodulator that is configured to receive the tuned satellite signal from said tuner and to perform demodulation on the tuned satellite signal, thereby outputting a demodulated data stream, wherein the demodulation is performed in accordance with control by a computer and wherein the computer monitors said demodulator;

a forward error corrector that is configured to receive the demodulated data stream from said demodulator and to perform forward error correction on the demodulated data stream, thereby outputting a corrected data stream;

an address filterer that is configured to receive the corrected data stream from said forward error corrector and to perform address filtering upon frames within the corrected data stream in accordance with an address header in each frame identifying the data stream to which the frame belongs, said address filterer outputting frames selected in accordance with the address filtering; and

an interface that is configured to receive the selected frames from said address filterer and to output the selected frames to the computer,

wherein said apparatus is an adapter card insertable into the computer and said apparatus outputs the selected frames to the computer through a bus interface,

wherein the antenna device further comprises a low noise block, and said connector is configured to provide power to the low noise block using power from the computer,

wherein said forward error corrector comprises a Viterbi decoder,

wherein said tuner is controlled by the computer.

15. An apparatus according to claim 14, wherein said connector is configured to provide power to the low noise block by using a DC-DC converter to convert power from the computer.

16. An apparatus according to claim 14, wherein said forward error corrector further comprises depuncture logic, a deinterleaver, and a Reed-Solomon decoder.

17. An apparatus according to claim 14, wherein said interface interrupts the computer to retrieve the selected frames.

18. An apparatus according to claim 14, wherein said interface automatically transfers the selected frames into memory of the computer.

19. An apparatus according to claim 14, wherein said connector is configured to provide power to the low noise block by using a DC-DC converter to convert power from the computer.

20. An apparatus according to claim 19, wherein the DC-DC converter provides power to said tuner.

21. An apparatus comprising:

a tuner that is configured to receive the output of a low noise block of a satellite receiver and perform tuning upon the output to select a signal;

a data demodulator that is configured to receive the signal selected by said tuner and to convert the signal into a digital data stream;

a power unit that is configured to receive power from a power supply of a computer and to output power to the low noise block of the satellite receiver through said tuner; and

an interface that is configured to transmit from said apparatus to the computer the digital data stream from said data demodulator and information for monitoring said data demodulator.

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22. An apparatus according to claim 21, wherein said interface is configured to allow the computer to send commands to said tuner and to said demodulator to control operation of said tuner and said demodulator.

23. An apparatus according to claim 21, further comprising a forward error corrector for performing forward error correction on the digital data stream.

24. An apparatus according to claim 21, further comprising means for identifying frames within the digital data stream.

25. An apparatus according to claim 21, further comprising means for performing address filtering to select frames from the digital data stream having a predetermined address.

26. An apparatus according to claim 21, further comprising means for performing decryption on a frame from the digital data stream.

27. An apparatus according to claim 21, wherein said apparatus is an adapter card insertable into the computer.

28. An apparatus according to claim 21, further comprising a microprocessor for controlling operation of said tuner and said demodulator.

29. An apparatus according to claim 21, wherein said power unit comprises a DC-DC converter that is configured to convert a voltage received from the power supply of the computer into a plurality of voltages.

30. An apparatus according to claim 29, wherein the plurality of voltages are received by said tuner and include a voltage for the low noise block.

31. An apparatus according to claim 30, wherein the voltage received from the power supply of the computer is +5V.

32. An apparatus comprising:

a tuner that is configured to receive the output of a low noise block of a satellite receiver and perform tuning upon the output to select a signal;

a data demodulator that is configured to receive the signal selected by said tuner and to convert the signal into a digital data stream;

a power unit that is configured to receive power from a power supply of a computer and to output power to the low noise block of the satellite receiver through said tuner; and

an interface that is configured to transmit from said apparatus to the computer the digital data stream from said data demodulator and information for monitoring said tuner.

33. A method comprising:

tuning the output of a low noise block of a satellite receiver to select a signal;

demodulating the signal selected in said tuning step into a digital data stream using a demodulator;

one of (a) transmitting to a computer through an interface the digital data stream and information for monitoring said demodulating step, and (b) transmitting to a computer through an interface the digital data stream and information for monitoring said tuning step; and

supplying power to the low noise block of the satellite receiver using a power supply of the computer.

34. A method according to claim 33, further comprising receiving commands via the interface for controlling said tuning and demodulating steps from the computer.

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35. A method according to claim 33, further comprising performing forward error correction on the digital data stream.

36. A method according to claim 33, further comprising identifying frames within the digital data stream.

37. A method according to claim 33, further comprising performing address filtering to select frames from the digital data stream having a predetermined address.

38. A method according to claim 33, further comprising decrypting a frame from the digital data stream.

39. An apparatus comprising:

means for powering a low noise block of a satellite receiver using power from a computer;

means for performing tuning and data demodulation upon the output of the low noise block to provide a digital data stream;

means for transmitting the digital data stream to the computer; and

means for transmitting information for monitoring the data demodulation to the computer.

40. An apparatus according to claim 39, wherein said means for powering comprises means for converting a voltage from a power supply of the computer into a voltage for the low noise block.

41. An apparatus according to claim 40, wherein said means for performing tuning and data demodulation comprises a tuner, and wherein the voltage for the low noise block is provided to the low noise block through said tuner.

42. An apparatus according to claim 21, wherein said power unit comprises a DC-DC converter that is configured to convert power from the power supply of the computer into a plurality of voltages.

43. An apparatus according to claim 42, wherein the plurality of voltages are received by said tuner and include a voltage for the low noise block.

44. An apparatus according to claim 32, wherein said power unit comprises a DC-DC converter that is configured to convert power from the power supply of the computer into a plurality of voltages.

45. An apparatus according to claim 44, wherein the plurality of voltages are received by said tuner and include a voltage for the low noise block.

46. A method according to claim 33, wherein said supplying step comprises a step of converting power from the power supply of the computer into a plurality of voltages.

47. A method according to claim 46, wherein the plurality of voltages are received by a tuner used in said tuning step and include a voltage for the low noise block.

48. An apparatus according to claim 39, wherein said means for powering comprises means for converting power from the power supply of the computer into a plurality of voltages.

49. An apparatus according to claim 48, wherein said means for performing tuning and data demodulation comprises a tuner, and wherein the plurality of voltages are received by said tuner and include a voltage for the low noise block.

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